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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *MT*

This application is a DIV of 10/201,892 07/25/2002 PAT 6,646,351

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *MT*

JAPAN 2001-230311 07/30/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR  COUNTRY JAPAN	SHEETS  DRAWING 6	TOTAL  CLAIMS 8	INDEPENDENT  CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>MT</i> Initials				

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## TITLE

Semiconductor device and manufacturing method thereof

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